GOVERNMENT POLYTECHNIC COLLEGE, PUNALUR

Phone: 9446181322
Email: gptcplr@gmail.com

MANDATORY DISCLOSURE

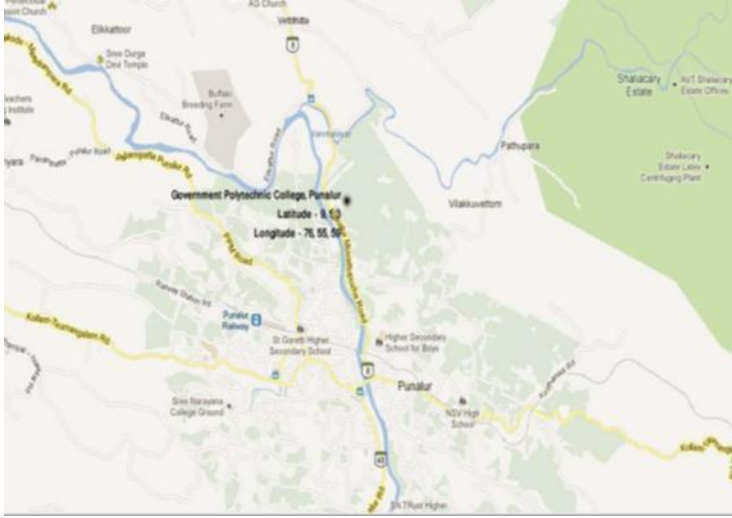|	
Mandatory Disclosure updated on	06th March 2023
AICTE File No.	South-West/1-10973536675/2022/EOADate:02-june-2022
Date & Period of last approval	Date: 02-06-2022, Academic Year 2022-23
Permanent Institute ID	:**1-500008781**

1. Name of the Institution

Name of the Institution	GOVT. POLYTECHNIC COLLEGE, PUNALUR
Address of the Institution	NELLIPPALLY, VALACODE P.O, PUNALURKOLLAM, PIN:691331,
City& Pincode	Punalur–691331
State / UT	Kerala
Longitude& Latitude	Latitude - 9°01'58.2"N Longitude- 76°55'28.2"E
Phone numbers	9446181322 / 8547840496
Office hours at the Institution	9.00AM to 4.00 PMfromMondayto Saturday
Academic hours at the Institution	9.00AM to 4.30 PM from Monday to Friday

Email	gptcplr@gmail.com
Website	www.gptcpunalur.org
Nearest Railway Station(distance in Km)	Punalur- – 3.6 Km
Nearest Airport(distance in Km)	Thiruvananthapuram International AirPort –76.1Km
Vision of the Institution	Be a centre of excellence in technical education with social and ecological commitment in service of nation
Mission of the Institution	To impart quality education with values among students. Equip youth to learn and apply technology in service to humanity. Train students with latest technological advancements, to develop entrepreneurship and meet the needs of industry

2. **Name and address of the Trust/ Society/ Company and theTrustees**

Name of the Parent Organization	Govt. Polytechnic College Punalur
Type of the Organization	Government
Registered With	Govt: of Kerala
Registration Date	08/25/2005
Registration Number	262/2005/RD

3. **Name and Address of the Vice Chancellor/ Principal/Director**

Name of principal	Dr.Saju Sankar S
Designation	Principal
Address	Nellippally, Valacode (P O),PunalurPin:691331, Punalur, Kollam.
Phone number with STD Code	9446181322
Email	sajusankars2019@gmail.cm

4. Name of the Affiliating University

Name of the Affiliating University	State Board of Technical Education,Kerala
Address	Thiruvananthapuram, Kerala
Website	www.sbte.kerala.gov.in,www.dtekerala.gov.in

5. Governance

Members of the Board and their brief background

Chairman	Dr. Anil Lal.S	Prof.of ME, CET TVM
Member (Industry)	Mr. Venugopal K. G	Dy GM, Kerala EI&Allied Eng. Co, Ltd, CHN
	Mr. S. Ratnakumar	Sr.man. (W), UnitedElectrical Industries, Kollam
Member (Institution)	Mr. AjithKumar N	HOD, ElectronicsEngineering
	Mrs. Sreevidya VS	Lecturer in Computer Engineering

Members of Academic Advisory Body(Academic Council)

Sl no	Name	Designation
1	Dr.Saju Sankar S	Principal
2	Mr.Ajith Kumar N	HOD in Electronics Engineering
3	Mr.Hameem Muhammed	Lecturer in Computer Engineering
4	Mr.Rahul R Nair	Lecturer in Electrical and Electronics Engineering
5	Mrs.Sreevidya VS	Lecturer in Computer Engineering
6	Mrs.Sreekala G	Assistant Prof in Chemistry
7	Mrs.Bindhu KS	Senior Superintendent
8	Mrs.Mayadevi	Vice president, PTA
9	Mr.Sudheer Kumar	Director, BALCO Pipes
10	Mr.Jyothi	Joint Secretary,PTA

Frequently of the Board Meeting and Academic Advisory Body   Once in every 2 weeks

Organisational Chart and processes

Student Feedback on Institutional Governance/ Faculty performance

There is an in-house mechanism to give feedback about the institutional governance and faculty performance

Grievance redressal mechanism for faculty, staff andstudents

Various Committees prescribed by AICTE/Government/Department are formed for Grievance redressal for students, faculty and staff.

<u>Establishment of Anti Ragging Committee</u>

Chairperson: Dr.SajuSankar S, Principal

Members:

Name Of The Committee Member	Profession	Mobile Number	E-Mail Address
Mr. Ajithkumar N	HOD, Electronics Engineering	9447438605	ajithkumarn@gmail.com
Mr. Hameem Muhammed	HOD, Computer Engineering (I/C)	7907413782	haameemgptc@gmail.com
Mr. Rahul R Nair	HOD In Electrical And Electronics Engineering (I/C)	9567647443	Rahulrnair1990@gmail.com
Smt. Bindhu K S	Senior Superintendent	9495266496	bindusivanandan@gmail.com

Smt.Vani.M	Lecturer in Computer Engineering	9400863347	vaniashish2012@gmail.com
Bahuleyan M	Local Media Reporter	9447991738	bahuleyanpachayil@gmail.com
Sri.SreejithSG	Lecturer in Electrical &ElectronicsEngineering	9496617492	jithussg@gmail.com
Sri. Rajeshkumar	Circle Inspector of Police, Punalur	9497987038	shopnlrklmrl.pol@kerala.gov.in
Sri. Rajan Mytreya	Manager, Ngo	4752335833	gurukulamcharity@gmail.com
Sri. PrakashT John	Demonstrator in Computer Engineering	9400728854	prakashtjohn@gmail.com
Smt. Maya devi	PTA Vice President	9744467396	pta_gptcplr@gmail.com
Mr. Jyothi	PTA Joint Secretary		pta_gptcplr@gmail.com
Mrs. Sandhya S	Parent		pta_gptcplr@gmail.com
Smt. Leijurani	Trade Instructor in Electronics Engineering	9947701639	lajulalu78@gmail.com
Sri. Hareeshkumar	Demonstrator In Elecrtical and Electronics Engg:	9496848362	harishporuvazy@gmail.com
Mrs. Kanmani B	Parent		pta_gptcplr@gmail.com
Smt. Baby Shereena Kohoor	Section Clerk	9496640284	shereenashefeeq@gmail.com
Sri. Adarsh.N Suresh	student, S6 Computer Engineering	8075475663	adarshnsuresh@gmail.com
Sri. Sachin D	student, S6 Electronics Engineering	9074756306	kgfrockybaidaaaa@gmail.com
Sri. Aswin Suresh	Student, S4 Electrical And Electronics Engineering	8075379313	Sureshaswin434@gmail.com

Establishment of Online Grievance Redressal Mechanism
http://www.gptcpunalur.org/grievences-registration-form

Establishment of Grievance Redressal Committee in the Institution

Sl no	Members	Designation
1	Dr. SajuSankar S	Principal
2	Mr.AjithKumar N	HOD in Electronics Engineering
3	Mr. Hameem Muhammed	HOD (i/c), Computer Engineering
4	Mr. Rahul R Nair	HOD in Charge, Electrical & Electronics Engineering
5	Mrs. Sreevidya V S	Lecturer in Computer Engineering

Establishment of Internal Complaint Committee (ICC)

Presiding Officer:

Ajith Kumar N, HOD in Electronics Engineering

Members:

Name of the Committee Member	Profession	Mobile Number	e-mail address
Mr. Hameem Muhammed	Hod, Computer Engineering (I/c)	7907413782	haameemgptc@gmail.com
Mr.RahulR Nair	HOD In Electrical and Electronics Engineering(I/c)	9567647443	Rahulrnair1990@gmail.com
Mrs. Chithra. S.R	MEMBER	9447084661	chithra.csr@gmail.com
Mrs. Sreevidya V S	MEMBER	8547840496	sreevidyajagadeesh@gmail.com
Smt Reshma. A.S	MEMBER	9744620034	reshmaas92@gmail.com
Smt. MaryMini	Demonstrator in Electronics Engineering	9496815872	minis72@gmail.com
Mr. Prakash TJohn	Demonstrator in Computer Engineering	9400728854	prakashtjohn@gmail.com
AswathyMohan	Student S4 CT	9567546698	Aswathymohan2107@gmail.com
AdarshNSuresh	Student S6 CT	8075379313	adarshnsuresh@gmail.com

Establishment of Committee for SC/ST

Convener:

Dr .Sreekala G, Asst. Professor in Chemistry

Members:

Name of the Committee Member	Profession	Associated with	Mobile Number	e-mail address
Smt. Chithra S R	Lecturer	Electrical and Electronics Engineering	9447084661	chithra.csr@gmail.com
Smt. Manju C C	Lecturer	Computer Engineering	9847449257	manjucc@gmail.com
Smt. Prasida .R	Assistant Professor	General Department	9809921140	prasi101@gmail.com
Smt. Molli B	Librarian	Office	9495408697	mollib@gmail.com

Internal Quality Assurance Cell

Sl no	Members	Designation
1	Dr. Saju Sankar S	Principal –chairperson
2	Sri. Ajithkumar N	HOD in Electronics Engineering-Director
3	Sri. Hameem Muhammad	HOD in Computer Engineering
4	Sri. Rahul R Nair	HOD in Electrical and Electronics Engineering
5	Sri.SreevidyaV S	Lecturer in Computer Engineering
6	Smt. Chithra S R	Lecturer in Electrical and Electronics Engineering
7	Smt. Jolly B	Lecturer In Electronics Engineering
8	Smt. Bindhu K S	Senior Superintendent
9	Sri. Shalu Krishnan R	Engineer(Knowledge ServicesGroup), KERALA STATE STEEL ECTRONICS DEVELOPMENT CORPORATION LTD
10	Sri.S.R Vinayakumar	Managing Director, United Electrical Industries Ltd

11	Sri. K S Unni	Principal, REGIONAL TELECOM TRAINING CENTRE, BSNL, TRIVANDRUM
12	Smt. Gracy John	Ward Councilor, Nellippalli, Punalur
13	Sri.Vivek S J	Infosys Pvt Ltd -Alumni
14	Nikhil Prakash	S4 Computer Engineering

6. **Programmes**

Name of Programmes approved by AICTE

Sl No	Name of Programme
1	Computer Engineering
2	Electronics Engineering
3	Electrical & Electronics Engineering

Name of Programmes Accredited by NBA　　　　　Nil

Status of Accreditation of the Courses

Total number of Courses	3
No. of Courses for which applied for Accreditation	Documentation process on going
Status of Accreditation	Preliminary

Details of Programmes

a) **Name of the Programme**　　　　**Electronics Engineering, Diploma, 3 Years**

Course	Diploma in Electronics Engineering		
Level	Diploma		
First Year of Approval by the Council			
Year-wise sanctioned Intake	60(2020)	60(2021)	60(2022)
Year-wise Actual admissions	60	60	60
% Students Passed with Distinction	14%	16%	16%
% Students Passed with First Class	32%	18%	11%
Students Placed	9%	7%	7%
Average Pay Package,Rs./Year	Rs 1,20,000/-	180000/-	225000/-
Students Opted for Higher Studies	10%	16%	8%

Accreditation Status of the Course	Not Accredited

b). Name of the Programme Electrical and Electronics Engineering, Diploma, 3 Years

Course	Diploma in Electrical and Electronics Engineering		
Level	Diploma		
First Year of Approval by the Council			
Year-wise sanctioned Intake	60(2020)	60(2021)	60(2022)
Year-wise Actual admissions	60	60	60
% Students Passed with Distinction	16.6%	26%	16.4%
% Students Passed with First Class	25%	10%	21.31%
Students Placed	2%	4%	
Average Pay Package, Rs./Year	Rs 96000/-	180000/-	3.2%
Students Opted for Higher Studies	4.1%	8%	120000/-
Accreditation Status of the Course	Not Accredited		

c). Name of the Programme   Computer Engineering, Diploma, 3 Years

Course	Diploma in Computer Engineering		
Level	Diploma		
First Year of Approval by the Council			
Year-wise sanctioned Intake	60(2020)	60(2021)	60(2022)
Year-wise Actual admissions	60	60	60
% Students Passed with Distinction	18%	9.6%	10%
% Students Passed with First Class	28.00%	38.4%	35%
Students Placed	20.0%	2%	20%
Average Pay Package,Rs./Year	Rs 2,00,000/-	250000/-	150000/-
Students Opted for Higher Studies	6%	12%	13%
Accreditation Status of the Course	Not Accredited		

Collaboration with   foreign universities   Nil

7. Faculty

Branch wise list of faculty members

Department of Computer Engineering – Faculty

Sl No	Name	Designation	Qualification
1	Sri. Hameem Muhammed	Head of Department	M.Tech
2	Dr. Manju C. C	Lecturer in Computer Engineering	M.Tech

3	Smt. Sreevidya V.S.	Lecturer in Computer Engineering	M.Tech
4	Smt. Vani. M.	Lecturer in Computer Engineering	B.Tech
5	Smt. Jeseena A	Lecturer in Computer Engineering	M.Tech
6	Smt. Soumya Surendran	Guest Lecturer in Computer Engineering	M.Tech

Faculty to Student ratio   1:25

Department of Electronics Engineering - Faculty

Sl No	Name	Designation	Qualification
1	Sri. AjithKumarN	Head of Department	M.Tech
2	Smt. Mini.C	Lecturer in Electronics Engineering	B.Tech
3	Smt. Jolly.B	Lecturer in Electronics Engineering	M.Tech
4	Sri. Binudas	Lecturer in Electronics Engineering	M.Tech
5	Smt. Reshma	Lecturer in Electronics Engineering	M.Tech
6	Sri. Jithin	Lecturer in Electronics Engineering	M.Tech

Faculty to Student ratio   1:25

Department of Electrical and Electronics Engineering - Faculty

Sl No	Name	Designation	Qualification
1	Sri. Rahul R Nair	Head of Department	M.Tech
2	Sri. Prince A Basheer	Lecturer in Electrical and Electronics Engineering	M.Tech
3	Smt. Chithra S R	Lecturer in Electrical and Electronics Engineering	M.Tech

4	Sri. Midhun M	Lecturer in Electrical and Electronics Engineering	M.Tech
5	Sri. Sreejith S G	Lecturer in Electrical and Electronics Engineering	M.Tech
6	Sri. Tony K John	Lecturer in Electrical and Electronics Engineering	B.Tech

Faculty to Student ratio   1:25

General Department - Faculty

Sl No	Name	Designation	Qualification
1	Dr. Prasida P	Assistant Professor of English	MA, PhD
2	Sri. Ignatius J	Assistant Professor of Physics	MSc, NET
3	Dr. Sreekala G	Assistant Professor of Chemistry	MSc, NET, PhD
4	Smt. Esakki Durachi	Assistant Professor of Mathematics(Guest)	MSc, BEd
5	Sri. ThomasV I	Instructor in Physical Education	BPE, MPEM Phil, PhD

8. Profile
a) Profile of Principal
   http://www.gptcpunalur.org/administration/1/Administrative-Staffs
b) Profile of Computer Engineering Faculty
   http://www.gptcpunalur.org/department/51/Computer-Engineering
c) Profile of Electrical Engineering Faculty
   http://www.gptcpunalur.org/department/50/Electrical-and-Electronics-Engineering
d) Profile of Electronics Engineering Faculty
   http://www.gptcpunalur.org/department/52/Electronics-Engineering
e) Profile of General Department Faculty
   http://www.gptcpunalur.org/department/53/Supporting-Department

9. Fees

Admission Fee	Rs 220 (At the time of Admission)
Tuition Fee	Rs 830 (Every Semester)
Special Fee	Rs 780 (Every Year, at thebeginning of odd semester)
P.D	Rs 600 (Every Year, at thebeginning of odd semester)
Students Amenities Fee	Rs 350 (Every Year, at the beginning of odd semester)
Caution Deposit	Rs 1000 (At the time of Admission)

Number of students, Fee Waivers offered	2020	2021	2022
	9	9	9

10. Admission

Admission details

Name of the Programme	COMPUTER ENGINEERING, DIPLOMA, 3 YEARS		
Year	2022	2021	2020
Sanctioned Intake	60	60	60
Actual Admissions	60	60	60
Name of the Programme	ELECTRICAL AND ELECTRONICS ENGINEERING,DIPLOMA, 3 YEARS		
Year	2022	2021	2020
Sanctioned Intake	60	60	60
Actual Admissions	60	60	60
Name of the Programme	ELECTRONICS ENGINEERING, DIPLOMA, 3 YEARS		
Year	2022	2021	2020
Sanctioned Intake	60	60	60
Actual Admissions	60	60	60

Management Quota	N.A.

11. Admission Procedure

Admission to various polytechnic colleges in the state through a Single Window Admission System. For Prospectus and other details visit: http://www.polyadmission.org

12. Criteria and Weightages for Admission

For details visit: http://polyadmission.org/files/prospectus-2018-en.pdf

13. List of Applicants

Rank List available in http://www.polyadmission.org

14. Results of Admission under Management seats/Vacant seats   N.A.

15. Information of Infrastructure and Other Resources Available

Infrastructure details

Number of Class Rooms and size of each	12,total area (681 sq.m)
Number of Tutorial rooms and size of each	2, total area (100 sq.m)
Number of Laboratories and size of each	14, total area (1210sq.m)
Number of Drawing Hall seating capacity of each	1,132(sq.m)
Number of Computer Centers with capacity of each	1 centre, 100computers,150m^2
Central Examination Facility, Number of rooms And capacity of each	Available, 1 room, 50m^2
Online examination facility (Number of Nodes, Internet bandwidth, etc.)	100Mbps,60
Barrier Free Built Environment for disabled and elderly persons	Available
Occupancy Certificate	Available
Fire and Safety Certificate	Available
Hostel Facilities-Boys	Available

Library

Number of Library books/ Titles/ Journals available	Number of Library books-10395
	Titles-2200
	Journals available-17
E- Library facilities	Available
National Digital Library(NDL) subscription details	NDLI Club Registration ID is: **INKLNC5Q3LGVJGS**

Computing Facilities

Internet Bandwidth in Mbps	100
Number of System	174
Total number of system connected by LAN	40
Total number of system connected by WAN	10
Major software packages available	
System Software:	GNU/LINUX, windows gcc, g++,java and Python
Application Software	Libre Office Writer, calc, impress, MySql, Postgres, Eclipse, Netbeans,Nasm, GIMP, ViEdior, Emacs, Koha, Apache, Tomcat, Openshot, Blender, Firefox, Chromium, VLC, GNU Debugger, Matlab
Special purpose facilities available (Conduct of online Meetings/Webinars/Workshops, etc.)	Gsuite,Google meet,moodle,webx
Facilities for conduct of classes/courses in online mode(Theory & Practical)	Moodle, Google meet, G suite
Innovation Cell	IEDC CELL-guides and assists students for attaining project approvals and facilities from

	agencies of support system
Compliance of the National Academic Depository (NAD), applicable to PGCM/ PGDM Institutions andUniversity Departments	N.A.

List of facilities available

Games and Sports Facilities	Outdoor sports facilities are available
Extra-Curricular Activities	National Service Scheme (NSS)activities College Arts Festival ED clubactivities IEDC Activities Inter Polytechnic Arts Festival
Soft Skill Development Facilities	Finishing School Programme Career Guidance and Placement Cell facilitate the soft skill development of students TeachingLearning Process

15.5 Teaching Learning Process

Curricula and syllabus for each of the Programmes as approved by the University	Computer Engineeringhttp://www.sitttrkerala.ac.in/index.php?r= site %2Fdiploma-syllabus-courses&prog=CT
	Electrical and Electronics Engineering http://www.sitttrkerala.ac.in/index.php?r=site%2Fdiploma-syllabus-courses&prog=EE
	Electronics Engineering http://www.sitttrkerala.ac.in/index.php?r=site%2 Fdiploma- syllabus-courses&prog=EL
Academic Calendar of the University	http://www.sitttrkerala.ac.in/index.php?r=site%2Fhome
Internal Continuous Evaluation System	Available
Student's assessment of Faculty, System in place	feedback

Academic Time Table with the name of the Faculty members handling the Course. Teaching Load ofeach Faculty

S2- TIME TABLE 2022-23 ELECTRONICS

	1	2	3	4	5	6
MONDAY	MAT	PHY	ENG	ENG	BE	FEE
TUESDAY	ES	PHY	BE	FEE	ENG	MAT
WEDNESDAY	MAT	ES	PHY		WS	
THURSDAY	MAT	ES	BE	FEE/THINK LAB		
FRIDAY	FEE/THINK LAB			FEE	PHY LAB	

S4- TIME TABLE 2022-23 ELECTRONICS

	1	2	3	4	5	6
MONDAY	LIC(3)	MC(4)	EMI(2)	LIC/PHY LAB(3,6)		
TUESDAY	EMI(2)	PCB/MC LAB(3,4)		LIC(3)	MC(4)	
WEDNESDAY	MC(4)	LIC(3)	EMI(2)	LIC/PHY LAB(4,6)		
THURSDAY	PCB/MC LAB(3,4)			CSIK(1)	MPROJ(1,2)	
FRIDAY	LIC(3)	MPROJ(1,2)	MC(4)	CSIK(1)	EMI(2)	MPROJ(1,2)

S6- TIME TABLE 2022-23 ELECTRONICS

	1	2	3	4	5	6	7
MONDAY	HDL/CHN LAB(5,6)		AMP(1)	CS(4)	CHN(6)	PRO & SEM(1,2)	
TUESDAY	CS(4)	CHN(6)	AMP(1)	RN(3)	HDL/CHN LAB(5,6)		
WEDNESDAY	CHN(6)	HDL/CHN LAB(5,6)		CS(4)	AMP(1)	PRO & SEM(1,2)	
THURSDAY	PRO & SEM(1,2)		AMP(1)	RN(3)	CHN(6)	CS(4)	RN(3)
FRIDAY	AMP(1)	CS(4)	CHN(6)	RN(3)	HDL/CHN LAB(5,6)		

Faculty work load

1	AJITH KUMAR N	AMP(5),PRO(6),CSIK(2),MPRO(4),BE(1)	18
2	BINUDAS S	THIN(6),EMI(4)PRO(6)MPRO(4)	20
3	MINI C	LIC(4),LICLAB(5),RN(4),PCB LAB(6)BE(1)	18
4	JOLLY B	MC(4),CS(5)MCLAB(6)LIC Lab(3)	18
5	RESHMA A S	HDL LAB(10),FEE(3),FEE LAB(6),BE(2)	21
6	JITHIN J S	CHN(5),CHNLAB(10),PHY(6),	21

Ajith Kumar N
HOD ELS

GOVERNMENT POLYTECHNIC COLLEGE PUNLAUR
DEPARTMENT OF COMPUTER ENGINEERING
TIME TABLE

Semester 6 — Batch:- 2022-2023

	1	2	3	4		5	6	7
MONDAY	SDP-V		MOBC-H		Lunch break	MC-SV	MC LAB-V,SV	SDP-V
TUESDAY	MC-SV		CN-J			MOBC-H	SDP LAB-V,SV	
WEDNESDAY	SDP-V		MOBC-H			PROJECT -H,J		
THURSDAY	MC LAB-V,SV					SEMINAR-H,S		
FRIDAY	CN-J		MC-SV			SDP LAB-V,SV		

Semester 4 — Batch:- 2022-2023

	1	2	3	4		5	6
MONDAY	WEB LAB -SV			DS-S	Lunch break	OOPS- M	
TUESDAY	CCN- V		DS LAB-S			OOPS-M	
WEDNESDAY	DS-S		AD LAB -M			CCN- V	CSIK-M
THURSDAY	MINOR PROJECT-J					CCN -V	CSIK-M
FRIDAY	WEB-SV		OOPS LAB-M			DS-S	

Semester 2 — Batch:- 2022-2025

	1	2	3	4		5	6
	9-10am	10-11am	11.10-12.10am	12.10-1.10pm		1.45-2.45pm	2.45-3.45pm
Monday	FEE	FEE	MATHS	PSP LAB		PSP LAB	
Tuesday	PHYSICS	maths	ES	ENGLISH		PHYSICS LAB	
Wednesday	PSP	MATHS	PSP	FEE LAB		FEE LAB	
Thursday	ENG	PHY	ES	ENGLISH	Lunch	MATHS	ES
Friday	FEE	PHY	PSP	WS	break	WS	

GOVERNMENT POLYTECHNIC COLLEGE PUNLAUR
Department of Electrical and Electronics

Semester 2 **Batch:- 2022-2025**

	1	2	3	4		5	6
	9-10am	10-11am	11.10-12.10am	12.10-1.10pm		1.45-2.45pm	2.45-3.45pm
Monday	EC (TKJ)		ES (Sreekala)	PHY (Ignatius)	Lunch break	FEC (CSR)	MAT (Essakki)
Tuesday	ENG	Workshop Practice				MAT (Essakki)	ES (Sreekala)
Wednesday	PHY (Ignatius)	ENG		MAT (Essakki)		PHYSICS LAB	
Thursday	FEE LAB/CAD LAB (SSG/TKJ)			PHY (Ignatius)		FEC (CSR)	MAT (Essakki)
Friday	EC (TKJ)	ES (Sreekala)	FEC (CSR)	FEE LAB/ CAD LAB (SSG/PAB)			

Subjcet Code	Subject Name	Staff Name
2001	Enviornmental Science	Sreekala G
2002	Mathematics II	Esakki
2003	Applied Physics II	Ignatius J
2031	Fundamentals of Electrical and Electronics	Chithra S R
2032	Elementary Concept of Electrical Systems	Tony K John
2006	Applied Physics Lab	Ignatius J
2008	Communication Skills in English lab	Prasida P
2039	Fundamentals of electrical and electronics Engg.Lab	Sreejith S G
2038	Engineering Graphics using CAD Software	Prince A Basheer, Tony K John
2009	Engineering Worksop Practice	Krishnankutti

Chithra S R
Lr. in EEE
(S2 class tutor)

Rahul R Nair
Lr. In EEE
(HOD in charge)

15/3/2023

Principal
Govt. Polytechnic College
Punalur - 691331

GOVERNMENT POLYTECHNIC COLLEGE PUNLAUR
Department of Electrical and Electronics

Semester - 4 **Time Table**

	1	2	3	4	5	6
Monday	IM Lab / DAR&M Workshop (MM) / (CSR)			EID (SSG)	Minor Project (SSG)	
Tuesday	IM (MM)	PE (CSR)	EID (SSG)	PP Lab / PE Lab (TKJ) / (CSR)		
Wednesday	PP Lab / PE Lab (TKJ) / (CSR)			EID (SSG)	Minor Project (SSG)	
Thursday	IM (MM)		PE (CSR)	IM Lab/DAR & M Workshop (MM)/(RRN)		
Friday	IM (MM)	PE (CSR)	EID (SSG)	PE (CSR)	Skill (CSR)	

PE-	Power Electronics Devices & Circuits
EID-	Electrical Installation Design & Estimation
IM-	Induction Machines
PE Lab-	Electronics Lab
IM Lab-	Induction Machines Lab
PP Lab-	Professional Practice Lab
DAR&M Lab-	Domestic Appliances Repair & Maintenance
Skill-	Community Skills in Indian Knowledge System

RRN-	Rahul R Nair
MM-	Midhun M
SSG-	Sreejith S G
CSR-	Chithra S R
TKJ-	Tony K John

15/3/2023
Principal
Govt. Polytechnic College
Punalur - 691331

GOVERNMENT POLYTECHNIC COLLEGE PUNLAUR
Department of Electrical and Electronics

Semester - 6 **Time Table**

	1	2	3	4		5	6	7
Monday	SM & FHP (PAB)		MC & PLC (RRN)	EPU (TKJ)	L U N C H	ACM Lab/ I A Lab (PAB / RRN)		
Tuesday	EPU (TKJ)	SM & FHP (PAB)	ACM Lab/ IA Lab (PAB / RRN)			Project (MM)		
Wednesday	I A (SSG)		MC & PLC (RRN)		B	ACM Lab/ I A Lab (PAB / RRN)		
Thursday	SM & FHP (PAB)		MC & PLC (RRN)		R E A K	I A (SSG)	EPU (TKJ)	
Friday	I A (SSG)	ACM Lab/ IA Lab (PAB / RRN)		EPU (TKJ)		Project (MM)		

SM & FHP-	Synchronous Machines & FHP Motors
EPU-	Electrical Power Utilization & System Peotection
MC & PLC -	Microcontrollers & Programmable Logic Controllers
I A-	Industrial Automation
I A Lab-	Industrial Automation Lab
ACM Lab-	AC Machines Lab

RRN-	Rahul R Nair
MM-	Midhun M
SSG-	Sreejith S G
PAB-	Prince A Basheer
TKJ-	Tony K John

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16. Enrolment details of students in the last 3years

2020_21	2021_22	2022_23
180	180	180

17. List of Research Projects/ Consultancy Works

Number of Projects carried out, funding agency, Grant receive N.A

Publications (if any) out of research in last three years out of masters projects -N.A.

Industry Linkage

Industry Institute cell

1	Sri. Midhun	Lecturer in Electrical and Electronics Engineering
2	Smt. Jolly B	Lecturer in Electronics Engineering
3	Smt. Chithra S R	Lecturer in Electrical and Electronics Engineering
4	Smt. Manju C C	Lecturer in Computer Engineering
5	Sri. Binudas	Lecturer in Electronics Engineering

MoUs with Industries

Sl no	Name of Industry
1	Kerala State Electronics Department Co-operation Ltd, (KELTRON)Thiruvananthapuram
2	Regional Telecom Training Centre ,(RTTC),BSNL Thiruvananthapuram.
3	Kera Fed Oil Complex, Kollam
4	Auto Kast Pvt Ltd, Alappuzha
5	Maks Automation Pvt Ltd Kottarakara
6	Solve Plastic Products Pvt Ltd
7	United Electrical Industries Ltd

18. LoA and subsequent EoA till the current Academic Year

http://www.gptcpunalur.org/academic/2/Certificates

19. Accounted audited statement

GOVT:POLYTECHNIC COLLEGE,PUNALUR

AUDITED STATEMENT OF ACCOUNTS OF 2022_23(INCOME&EXPENDITURE STATEMENT)

SI No	INCOME(in lakh) Details	Amount	EXPENDITURE(in lakh) Details	Amount
1	Income From Central Govt.	NIL	Salary Teaching Staff:	257.67
2	Income From State Govt.:	NIL	Salary Non-teaching Staff:	206.68
3	Income From Student Fees:	NIL	Library:	NIL
4	Income From Donations:	5.7	Equipment:	3.58
5	Income From UGC:	NIL	Building Maintainance:	NIL
6	Income From Other Bodies/Sources:	NIL	Other Expenditure:	8.42
8	**Total Income:**	5.7	**Total Expenditure:**	476.35

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Audited Statement of Account of 2019-20 (Income & Expenditure Statement for complete year)

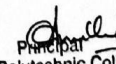| Income SI.No.	Details	Amount	Expenditure SI.No.	Details	Amount
1	Tuition Fee	263970	1	Salary	38893266
2	Other fee/amount collected from students	704233	2	Administrative expenses	
3	Grants from Govt/Private agencies		3	Training and Development	
4	Grants/Contribution from other sources		4	Laboratory consumables	
5	Scholarships received		5	Library	
6	Other Income		6	Travel	
			7	Fees Paid to University/board/Government/AiCTE/UGC	
			8	Repairs and Maintenance	
			9	Scholarships/Concessions/Fellowships/Honorarium et., awarded/incurred(other than Govt. grants	
			10	Expenditure of grants received from Govt./Private agencies	
			11	Depreciation	
			12	Any other expenditure	453563
	Total	968203		**Total**	39346829

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Audited Statement of Account of 2020-21(Income & Expenditure Statement for complete year)

	Income			Expenditure	
Sl. No.	Details	Amount	Sl. No.	Details	Amount
1	Tuition Fees	279397.00	1	Salary	29163850.00
2	Other fee/amount collected from students	732305.00	2	Administrative expenses	NIL
3	Grants from Govt. / Private agencies	NIL	3	Training and Development	NIL
4	Grants / Contribution from other sources (Management)	NIL	4	Laboratory consumables	11530.00
5	Scholarships received	NIL	5	Library	NIL
6	Other income	NIL	6	Travel	9260.00
			7	Fees Paid to University/ Board/ Government/AICTE/UGC	NIL
			8	Repairs and Maintenance	
			9	Scholarships/ Concessions/ Fellowships/Honorarium etc., awarded/incurred(other than Govt. grants)	
			10	Expenditure of grants received from Govt. / Private agencies	
			11	Depreciation	
			12	Any other expenditure	1642143.00
		Total 1011702.00			Total 30826783.00

Auditor

Principal

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Punalur - 691331

20. Best Practices adopted, if any

- The Young Innovators Programme (YIP) is a flagship program of the Government of Kerala launched by the Kerala Development and Innovation Strategic Council (K DISC) to promote a culture of innovation among the youths of Kerala. Our students group actively participated by submitting their innovative ideas in YIP-2021 and YIP-2022.

- Also Community Development programmes [CDTP]are conducted in 8 extension centers providing training to the underdeveloped areas thereby rural employability is entrusted in traditional jobs as well as in new areas.

- Innovation and Entrepreneurships Development centers [IEDC] platform is set up to promote innovative ideas of students and encourages them to present in technical exhibitions.

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